

FIG. 3

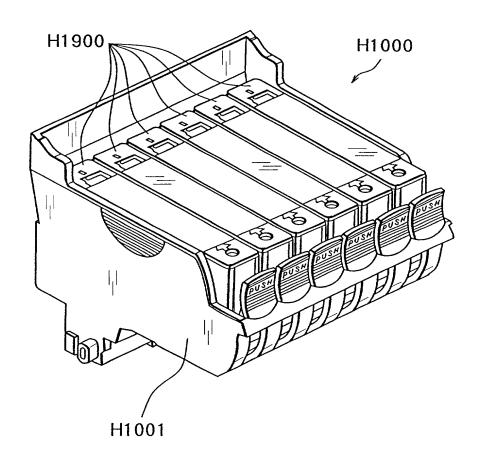
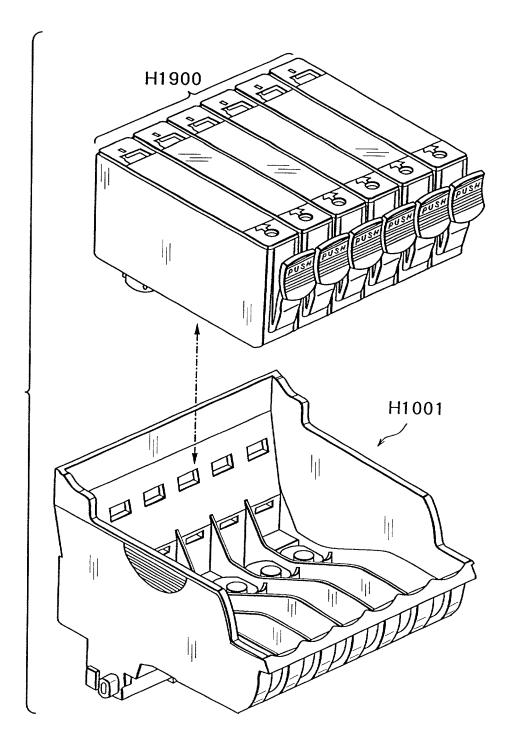
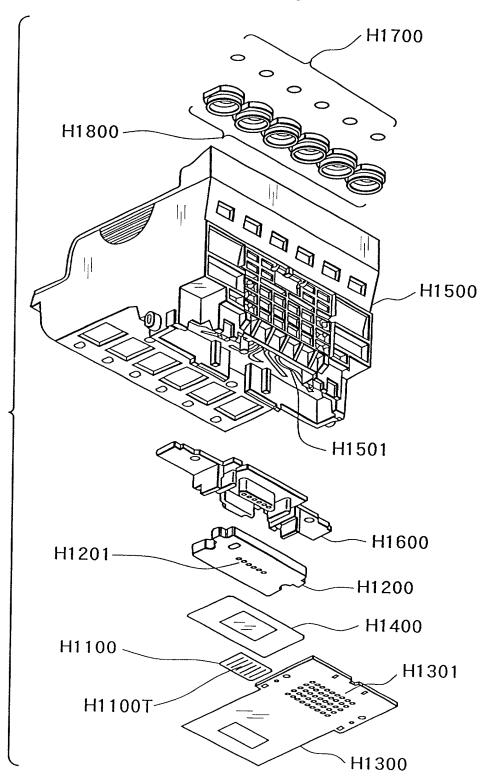


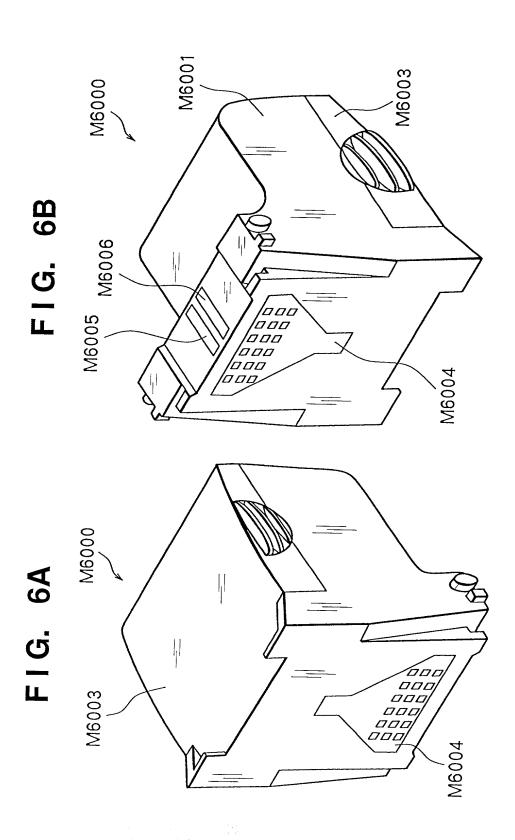
FIG. 4



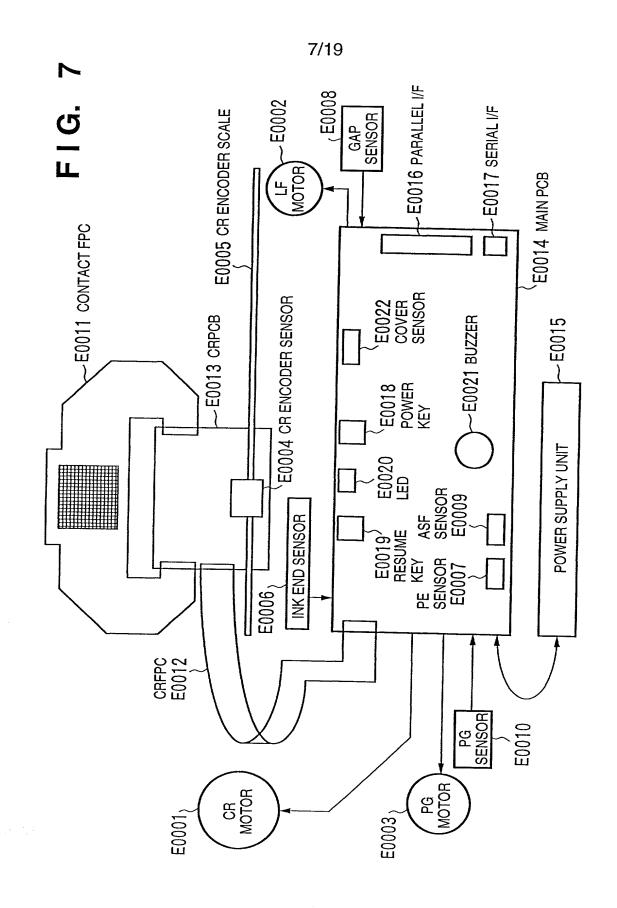
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FIG. 5

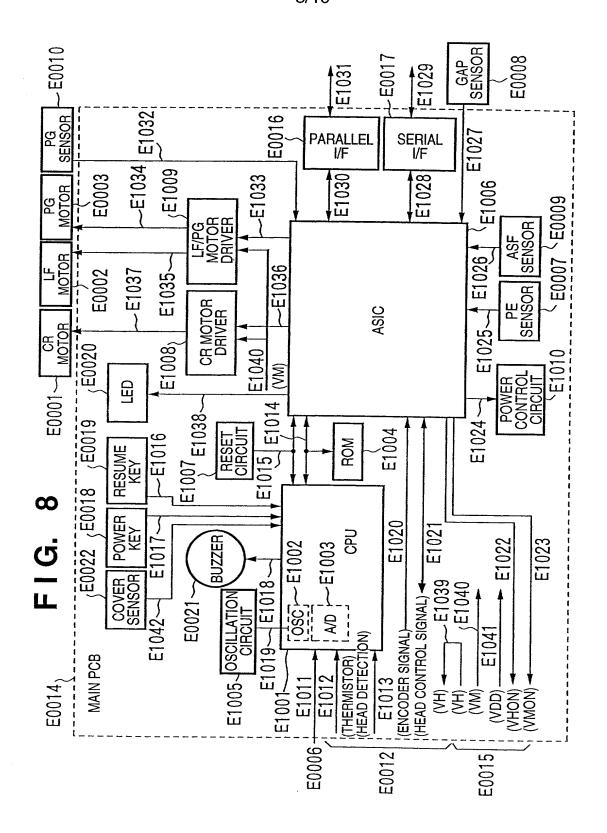


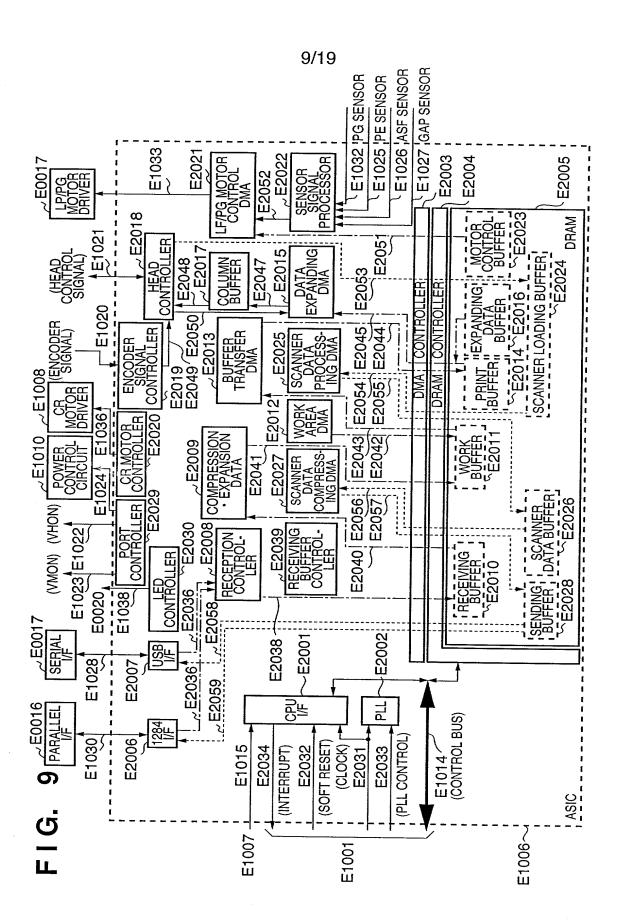


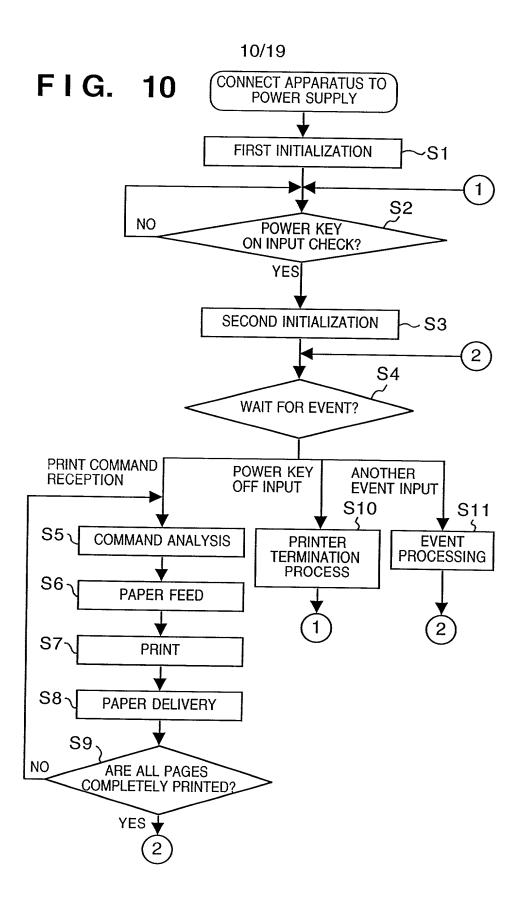
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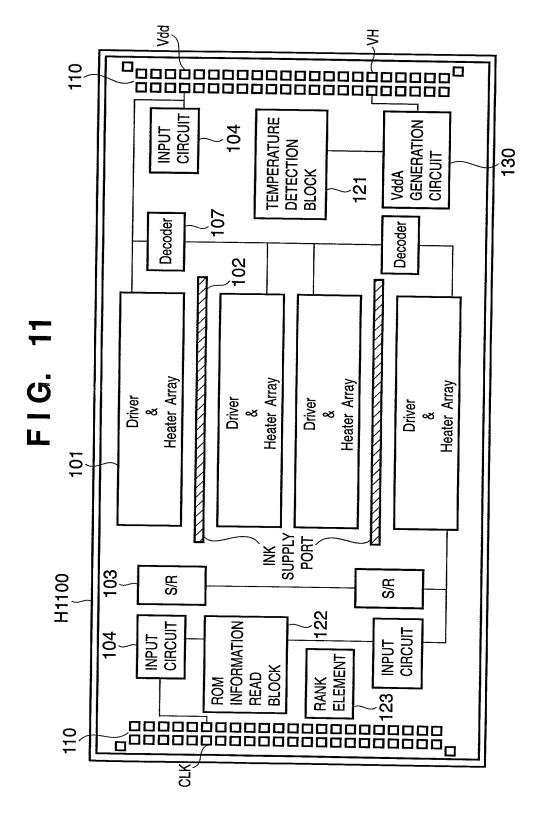


FIG. 12

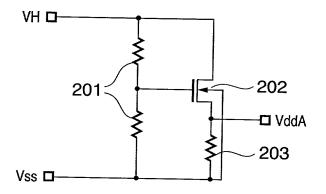
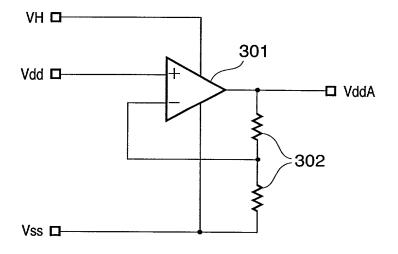
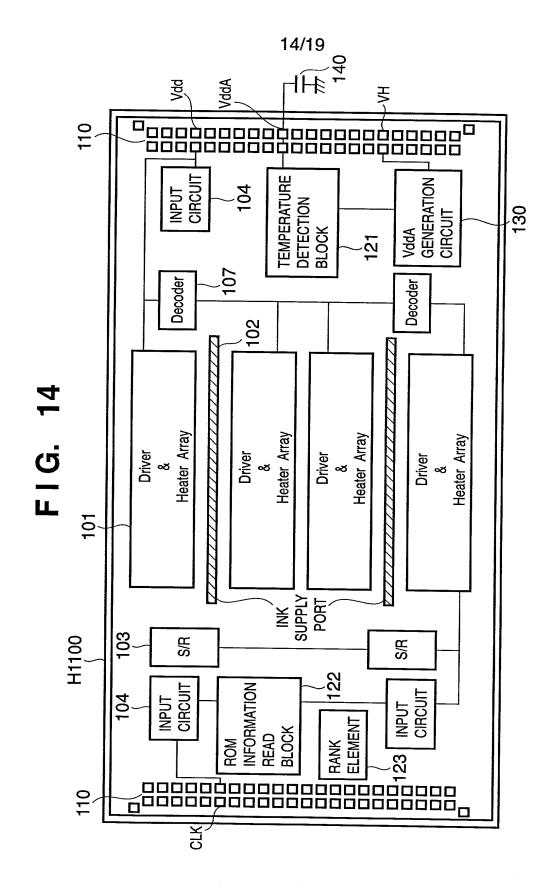


FIG. 13





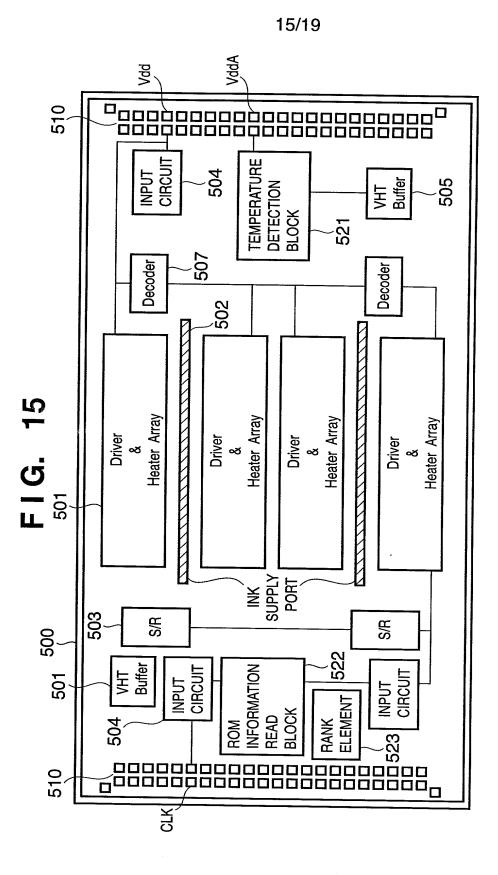


FIG. 16A

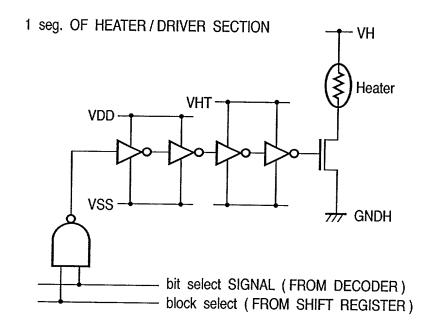
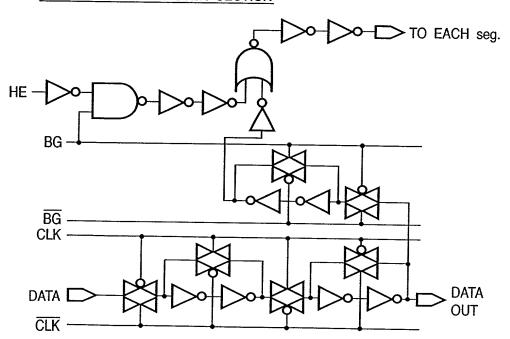
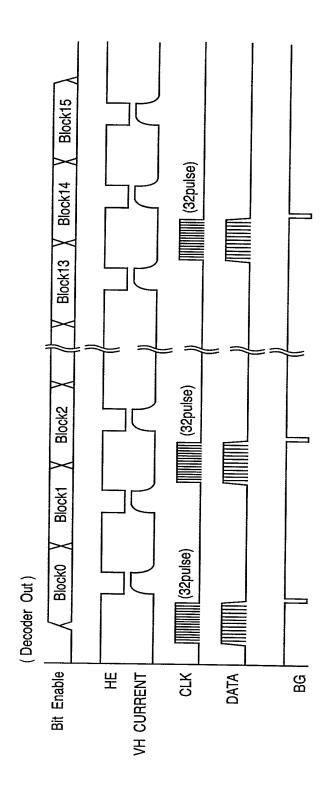


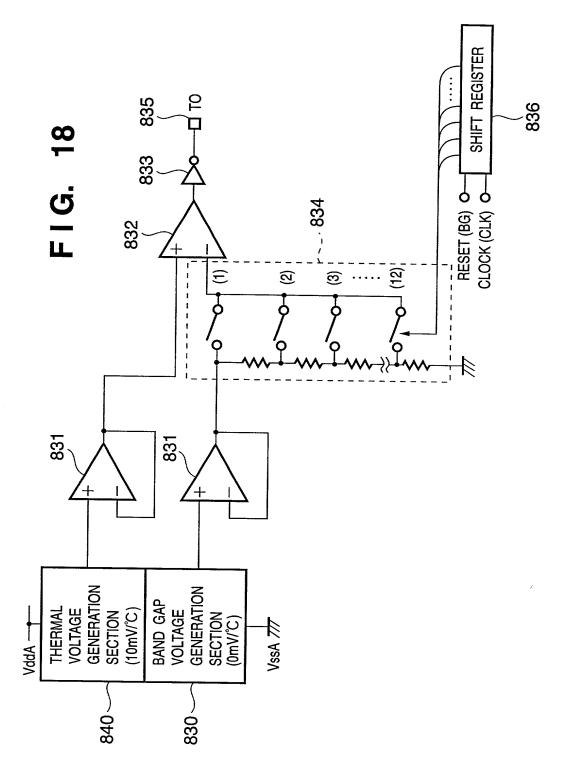
FIG. 16B

1 bit OF SHIFT REGISTER SECTION



F.G. 17





F. G. 19

